

Figure 1A

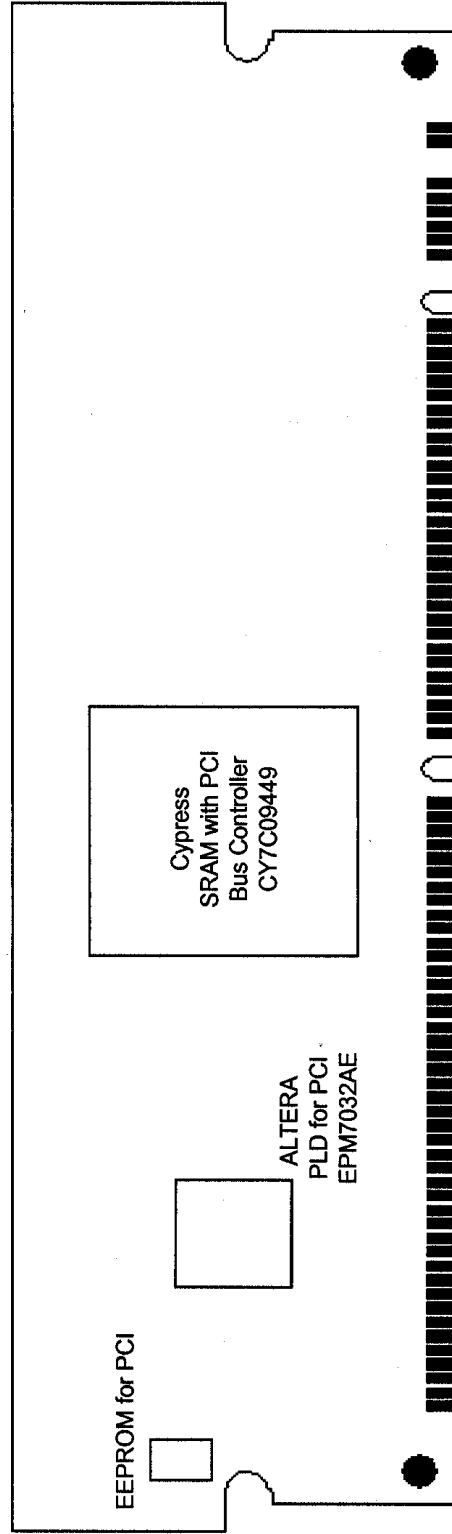


Figure 1B

Backplane Layout

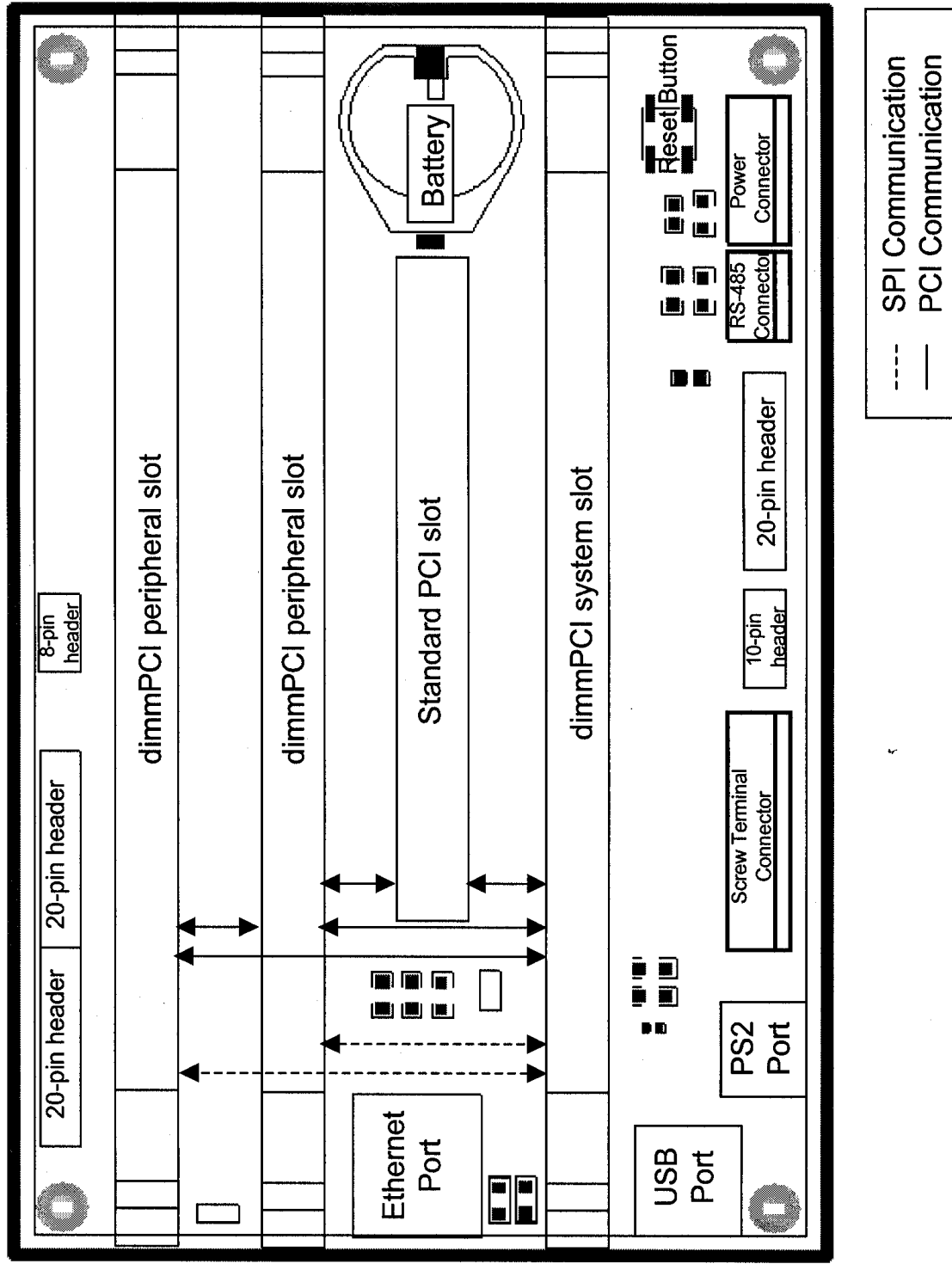


Figure 2

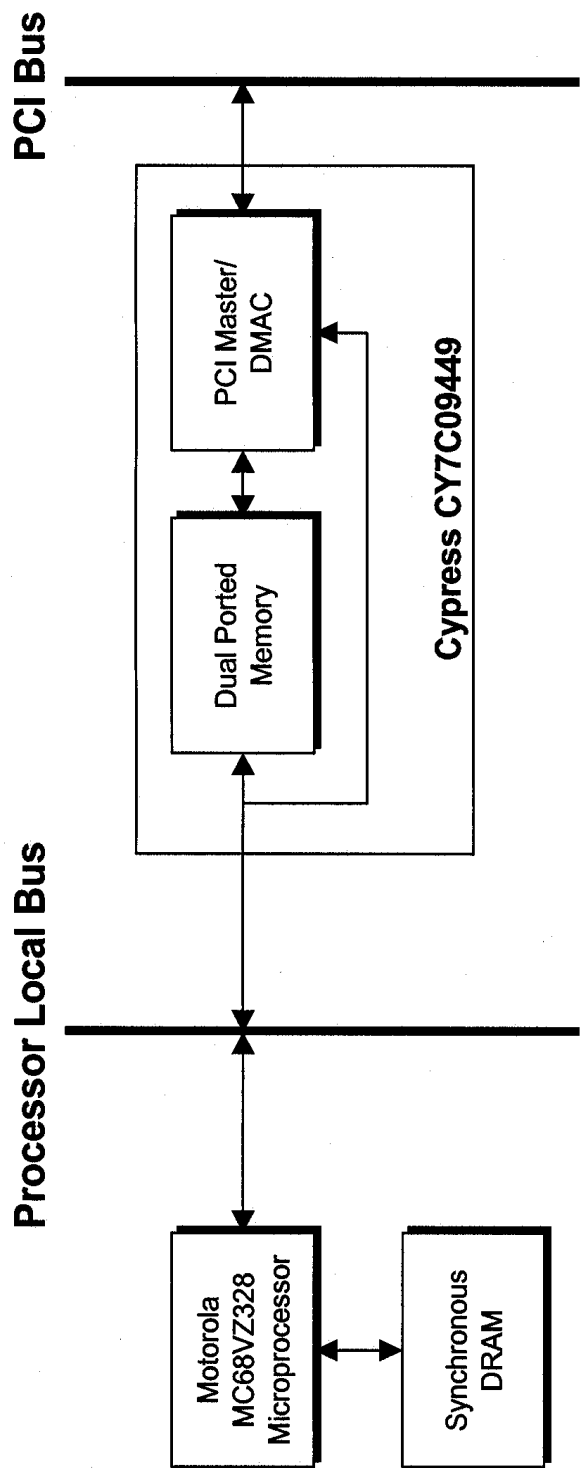


Figure 3

PCI Bus

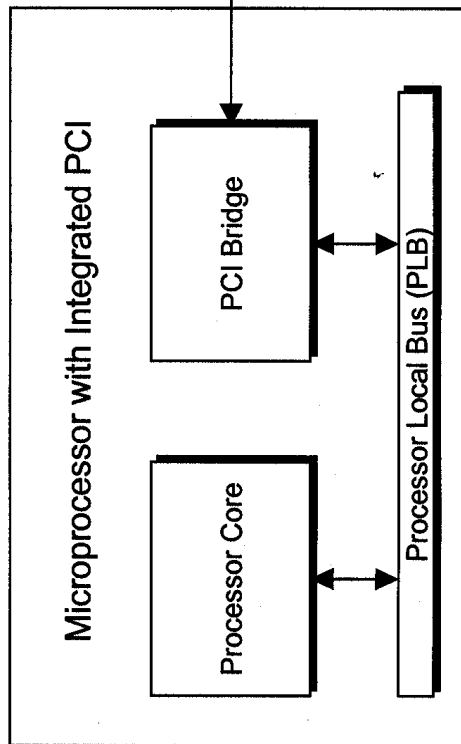


Figure 4

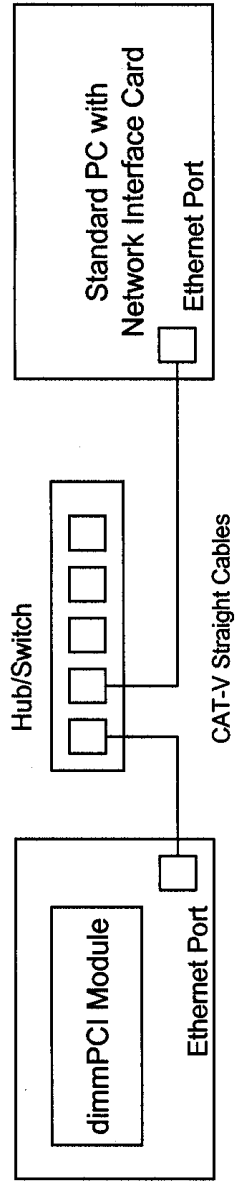


Figure 5

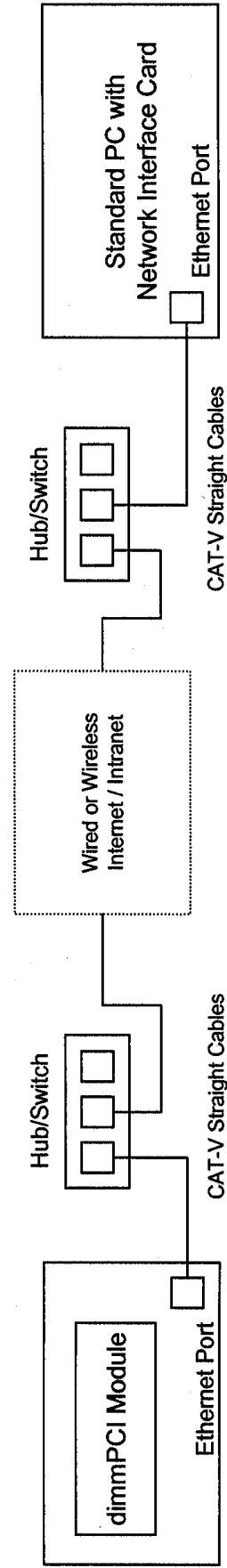


Figure 6